

**IN THE SPECIFICATION**

Please amend the specification as follows:

Please insert before the first line of the specification the following:

D -- This is a continuation of application Serial No. 09/261,179, filed March 3, 1999, now U.S. Patent No. 6,438,358. --

Please replace the paragraph beginning on page 7, line 20 with the following rewritten paragraph as follows:

TD2 -- The signal band of this reception intermediate frequency signal is limited to 1.25 MHz by using an RX IF band-pass filter 17, and thereafter the limited reception-IF signal is amplified by an RX variable gain amplifier 18 so as to obtain a necessary signal level there of in the post-staged circuit. Then, the amplified IF signal is 4-phase-demodulated by an RX second mixer 21 of a base-band analog signal processing circuit 67. Moreover, the frequency band of this reception base-band signal is limited by an RX low-pass filter 24, and then the band-limited reception base-band signal is converted into a reception digital signal by an analog-to-digital converter 25. This digital signal is entered into a CDMA demodulator 26 of a base-band digital signal processing circuit 7 provided at a post stage of the A/D converter 25. --

Please replace the paragraph beginning on page 8, line 25, with the following rewritten paragraph as follows:

TD3 -- In a transmission signal systems, transmission voice is picked up by a microphone 9, the voice signal is PCM-coded by the codes 52, and then the PCM-

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coded voice signal is processed by the vocoder 51 by way of the high efficiency speech coding to produce a transmission voice signal. This transmission voice signal and the control signal produced by the controller 54 are processed by the CDMA ~~demodulator~~ modulator 46 by way of the convolution coding, the block interleaving, the 64-order quadrature modulating, and the direct sequence spreading, so that a transmission digital signal. Then, this transmission digital signal is supplied to a TX digital-to-analog converter 45 of a base-band analog signal processing circuit 6 provided at the next stage of the CDMA modulator 46. --

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